



CYPR-CD00055

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Technology Center 2100

Patent

2172

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of)

Merchant, et al.)

Serial No. 09/684,160)

Filing Date: October 4, 2000)

For: Method and System for)
Generating A Bit Order Data)
Structure Of Configuration Bits)
From A Schematic Hierarchy)

Examiner: Hamilton, M.G.

Art Unit: 2172

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE TO OFFICE ACTION

Dear Sir:

In response to the Office Action mailed July 6, 2004, the following amendments and remarks to the above captioned patent application are respectfully submitted. Reconsideration of the above captioned patent application is respectfully requested.

Serial No. 09/684,160
Examiner: Hamilton, M.G.

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